






**Digital computer having signal circuitry.**




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**Applicant:** AUTOMATION SYST (US)  
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 EP0102152 (A2)  
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 EP0308583 (A3)  
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**Cited documents:**

 US4064394  
 US3587055  
 US3243582

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**Abstract of EP0308583**

A combination of a programmable logic controller with analog circuitry. The analog circuitry includes a summation point to which several items are coupled. Analog inputs are selectively coupled to the summation point through analog switches. Also, the output of a digital to analog converter couples to the summation point. Still further, the analog output for the controller is obtained from a sample and hold circuit which has its input connected to the summation point and which includes means for outputting the analog value at its output back to the summing point. Even still further, a comparator input couples to the summation point. The arrangement provides for direct processing of analog information either by direct output of analog processed analog data or by obtaining one bit data from the comparator which represents whether a threshold has been reached by the analog data. Digital processing of the analog data may be accomplished, if necessary by using the circuit to convert from analog to digital and back again. The equipment is designed so that digital or analog, input or output cards may be inserted into any of the I/O positions without rewiring.

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